



VLSI TRAINER

MODEL - VLSI101P

This trainer has been designed with a view to provide practical and experimental Knowledge of VLSI Design technique on a P.C.B.



SPECIFICATIONS

1. VLSI Trainer
2. **Books for VLSI Lab** : **10 Nos in pdf Format**
3. **Mp4 Video Class for VLSI Lab** : **40 Classes in Mp4 on Pen Drive**

EXPERIMENTS

* **To prepare CMOS layout in selected technology, simulate with and without capacitive load, comment on rise, and fall times.**

1. Inverter, NAND, NOR gates, Half Adder
2. 2:1 Multiplexer using logic gates and transmission gates.
3. Single bit SRAM cell.
4. D flip-flop.

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Dealer:-